

1 Inventor: John T. Moore

2 Title: Capacitor Constructions, DRAM Constructions, and  
3 Semiconductive Material Assemblies (As Amended)

4 Assignee: Micron Technology, Inc.

#3/105  
11/1/00  
Junker

5 **INFORMATION DISCLOSURE STATEMENT**

6 **PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

7 In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is  
8 directed to the United States patents and other references listed on the  
9 attached Form PTO-1449. No admission is made regarding whether all the  
10 submitted references are prior art.

11 The listed references were cited by, or submitted to, the Office in the  
12 parent, co-pending application of the above-identified application. The above-  
13 identified application is a divisional application of co-pending application  
14 Serial No. 09/175,051, filed October 19, 1998. Such prior disclosure is  
15 sufficient for the above-identified application as far as copies of the references  
16 are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

17 Citation of these references is respectfully requested.

18 Respectfully submitted,

19  
20 Dated: June 23, 2000

21 Attorney: 

22 David G. Latwesen, Ph.D.

23 Reg. No. 38,533

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